



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Kiyoshi Arita
Serial No.: 10/716,965
Filed: November 19, 2003
Title: "METHOD OF MANUFACTURING SEMICONDUCTOR
DEVICE PLASMA PROCESSING APPARATUS AND
PLASMA PROCESSING METHOD"
Docket No.: 36280

LETTER

Mail Stop MISSING PARTS
Commissioner of Patents
P.O. Box 1450
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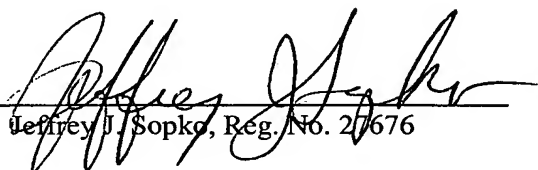
Sir:

Enclosed herewith are 11 sheets of formal drawings for filing in the above-identified application. Please note that applicant has not yet received a Notice to File Missing Parts of Application.

Respectfully submitted,

PEARNE & GORDON LLP

By


Jeffrey J. Sopko, Reg. No. 27676

1801 East 9th Street
Suite 1200
Cleveland, Ohio 44114-3108
(216) 579-1700

January 6, 2004

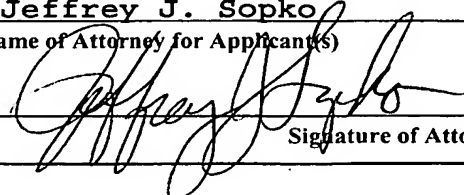
I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Mail Stop Missing Parts, Commissioner of Patents, P.O. Box 1450 Alexandria, Va. 22313-1450 on the date indicated below.

Jeffrey J. Sopko

Name of Attorney for Applicant(s)

01/06/04

Date



Signature of Attorney